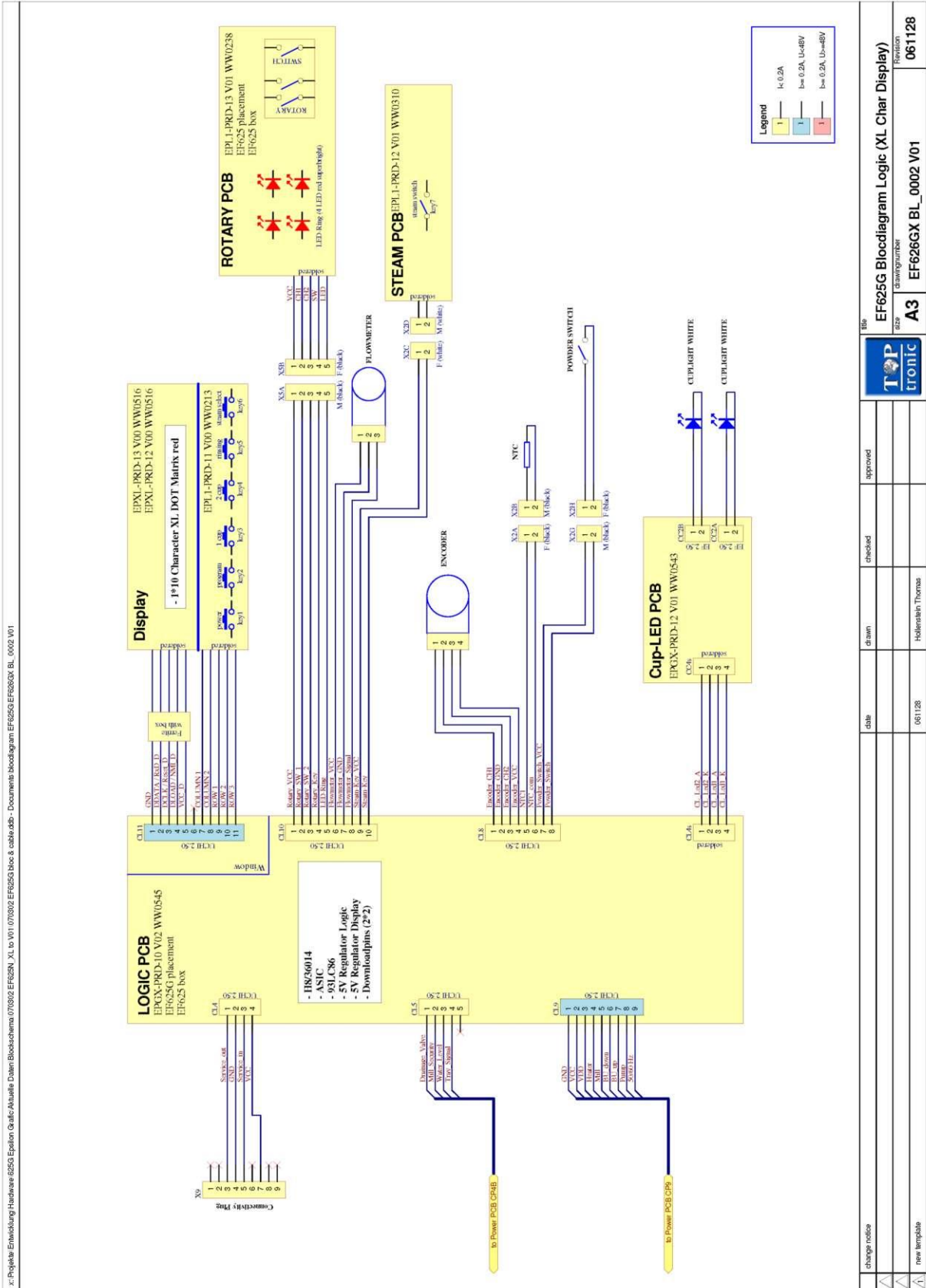




(2) Scheme logic



(3) Scheme power

